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UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,787,383 B2  
DATED : September 7, 2004  
INVENTOR(S) : Shunichi Ikeda et al.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 18,

Line 11, delete " $0 \leq x \leq 1$  and  $x + Y \leq 1$ " and substitute therefore

--  $0 \leq x \leq 1$ ,  $0 \leq y \leq 1$  and  $x + Y \leq 1$  --

Line 17, after " $(\text{Al}_x\text{Ga}_{1-x})_y\text{In}_{1-y}\text{P}$ " insert -- or  $\text{In}_x\text{Ga}_y\text{Al}_{1-x-y}\text{N}$ , --

Column 19,

Line 11, delete " $0y \leq 1$ " and substitute therefore --  $0 \leq y \leq 1$  --

Line 53, delete " $\text{In}_x\text{Ga}_y\text{Al}_{1-x-y}\text{N}$ " and substitute therefore --  $\text{In}_x\text{Ga}_y\text{Al}_{1-x-y}\text{N}$  --

Column 20,

Line 38, delete " $0y \leq 1$ " and substitute therefore --  $0 \leq y \leq 1$  --

Line 41, delete "claim 1" and substitute therefore -- claim 16 --

Line 44, delete " $\text{C}_3$ " and substitute therefore --  $\text{C}_B$  --

Line 59, delete " $0y \leq 1$ " and substitute therefore --  $0 \leq y \leq 1$  --

Signed and Sealed this

Nineteenth Day of April, 2005



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*